WHAT IS CLAIMED IS:

1.   A computer system comprising:

a memory comprising a host and an image file;

a processor configured to execute the host;

an input / output (I/O) controller coupled to the processor; and

a management processing system coupled to the I/O controller and comprising a first storage location, a first memory, and a second memory;

wherein the host is configured to cause the processor to store a first portion of the image file in the first memory, wherein the host is configured to cause the processor to set the first storage location subsequent to storing the first portion in the first memory, and wherein the management processing system is configured to store the first portion of the image file in the second memory in response to detecting that the first storage location has been set.

2.   The computer system of claim 1 wherein the management processing system comprises a second storage location, and wherein the management processing system is configured to set the second storage location subsequent to storing the first portion of the image file in the second memory.

3.   The computer system of claim 2 wherein the management processing system is configured to store information in the first memory prior to setting the second storage location.

4.   The computer system of claim 3 wherein the information comprises an acknowledge message.

5.   The computer system of claim 2 wherein the host is configured to cause the processor to store a second portion of the image file in the first memory in response to detecting that the second storage location has been set, wherein the host is configured to cause the processor to set the first storage location subsequent to storing the second portion in the first memory, and wherein the

management processing system is configured to store the second portion of the image file in the second memory in response to detecting that the first storage location has been set.

6. The computer system of claim 2 wherein the first storage location comprises a first register, and wherein the second storage location comprises a second register.

7. The computer system of claim 1 wherein the management processing system comprises a network connection that is configured to provide a remote user with access to the computer system.

8. The computer system of claim 7 wherein the management processing system is configured to provide status information associated with the computer system to the remote user.

9. The computer system of claim 1 further comprising:

a PCI bus coupled to the I/O controller and the management processing system;

wherein the I/O controller comprises a PCI controller.

10. The computer system of claim 1 wherein the host is configured to be executed by the processor subsequent to an operating system being booted by the processor.

11. The computer system of claim 1 wherein the host is configured to be executed by the processor prior to an operating system being booted by the processor.

12. The computer system of claim 1 wherein the host is configured to be executed by the processor using an Extensible Firmware Interface (EFI) protocol.

13.   The computer system of claim 1 wherein the host is configured to be executed by the processor using an Intermediate System Loader (ISL) protocol.

14.   The computer system of claim 1 wherein the processor is configured to cause the portion of image file to be provided to the management processing system using a plurality of messages, and wherein each of the plurality of messages comprises a header and a body.

15.   The computer system of claim 1 wherein the image file comprises firmware.

16.   The computer system of claim 1 wherein the second memory comprises a non-volatile memory.

17.   A system comprising:
        a master;
        a slave;
        a bus coupled to the master and the slave;
        a first storage location;
        a second storage location; and
        a first memory accessible to the master and the slave;
        wherein the master is configured to store first information in the first memory using the bus, wherein the master is configured to set the first storage location subsequent to storing the first information using the bus, wherein the slave is configured to access the first information in response to detecting that the first storage location has been set, wherein the slave is configured to store second information in the first memory subsequent to accessing the first information, wherein the slave is configured to set the second storage location subsequent to storing the second information, and wherein the master is configured to access the second information using the bus in response to detecting that the second storage location has been set.

18. The system of claim 17 wherein the bus comprises a PCI bus.

19. The system of claim 17 wherein the master comprises a host.

20. The system of claim 17 wherein the slave comprises a management processing system.

21. The system of claim 17 wherein the first storage location comprises a first register, and wherein the second storage location comprises a second register.

22. The system of claim 17 wherein the first information comprises at least a portion of a firmware upgrade.

23. The system of claim 22 wherein the second information comprises an acknowledge message.

24. A method performed by a computer system that comprises a management processing system coupled to an input / output (I/O) bus comprising:

storing first information into a first memory in the management processing system using the I/O bus;

setting a first storage location in the management processing system to a first value using the I/O bus;

accessing the first information in response to detecting that the first storage location has been set to the first value;

storing second information into the first memory in response to accessing the first information in the first memory; and

setting a second storage location in the management processing system to a second value subsequent to storing the second information.

25.   The method of claim 24 storing the first information into a second memory in response to accessing the first information.

26.   The method of claim 25 wherein the first information comprises firmware, and wherein the second memory comprises a non-volatile memory.

27.   The method of claim 24 further comprising:

storing third information into the first memory using the I/O bus in response to accessing the second information; and

setting the first storage location in the management processing system to the first value using the I/O bus subsequent to storing the third information.

28.   The method of claim 27 further comprising:

accessing the third information in response to detecting that the first storage location has been set to the first value;

storing fourth information into the first memory in response to accessing the first information in the first memory; and

setting a second storage location in the management processing system to a second value subsequent to storing the second information.

29.   The method of claim 28 wherein the first information comprises a start upgrade message, wherein the second information comprises a first acknowledge message, wherein the third information comprises at least a portion of an image file, and wherein the fourth information comprises a second acknowledge message.